THE UNIVERSITY OF MELBOURNE

RAMP		SATURATED ABSORPTION		DITHER		LOCK-IN		CONTROL			
	+ RISE TIME										+ MONITOR STATUS
+	+ FALL TIME	+	+	+ FREQ	+ AMPL	+	+	+	+	+ INTEG BALANCE	
RANGE EXT		OFFSET	SAT/REF/DIFF	ON	EXTERNAL	PHASE	OFFSET	PROP GAIN SCAN	INTEG GAIN INTEG ON	INTEG (+)	
+ INT	+ EXT RAMP IN	+ GAIN	+ OUT	+ OFF	+ INTERNAL	+ INPUT GAIN	+ OUTPUT GAIN AC LOCK	+ LOCK	+ INTEG OFF	+ INTEG (-)	+
+ OUTPUT	+ TRIG OUT	+ REF GAIN	+ REF OFFSET	+ DITH IN	+ DITH OUT	+ ERROR	+ BYPASS	+ PROP OUT	+ INTEG OUT		POWER

CLAUDIUS

MONITOR

POWER

MONITOR PINOUTS

1. RAMP
2. GND
3. TRIG
4. GND
5. SAT ABS
6. GND
7. DITHER
8. GND
9. DIFF ABS
10. GND
11. ERROR
12. GND
13. INTEG
14. GND
15. INDICATOR

LINK

LINK PINOUTS

1. DISK +
2. DISK –
3. STACK +
4. STACK –

PHOTO DIODE

SAT ABS

REF ABS

PHOTO DIODE PINOUTS

1. V +
2. V –
3. GND
4. SAT ABS
5. REF ABS

CLAUDIUS